UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 6,914,374 B2 Page 1 of 1

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INVENTOR(S) : Henryk Birecki et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 9, line 5, in Claim 1, after "surface of" insert -- a semiconductor layer above --.

In column 9, line 6, in Claim 1, after "wherein the" insert -- exposed surface of the --.

In column 9, line 9, in Claim 1, after "of the" insert -- exposed surface of the --.

In column 10, line 8, in Claim 8, after "configured" delete "to" and insert -- to have a variable thickness, to thereby --, therefor.

Signed and Sealed this

Sixteenth Day of March, 2010

David J. Kappos Director of the United States Patent and Trademark Office